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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 11/14/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TX	SHEETS DRAWING 10	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 2
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Verified and Acknowledged

Examiner's Signature: *[Signature]* Initials: *[Initials]*

ADDRESS  
 45327  
 IBM CORPORATION (CS)  
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TITLE  
 Clock signal selector circuit with reduced probability of erroneous output due to metastability

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